

**Notice of References Cited**

Application/Control No.

09/760,063

Applicant(s)/Patent Under  
Reexamination  
HARER ET AL.

Examiner

Thomas H. Stevens

Art Unit

2123

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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,600,579	02-1997	Steinmetz, Jr., William C.	703/13
	B	US-			
	C	US-			
	D	US-			
	E	US-			
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	I	US-			
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	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	Ho-P.H. et al. "Smart Simulation using Collaborative Formal and Simulation Engines". IEEE (11/09/00). CAD conference.pg. 120-126.			
	V	Harer-K. "Design and Maintenance Specification for CTG Reachability & Control Subsystem. Version 1.0 (2/14/00). pg. 1-50			
	W	Bertacco-V. et al. "Cycle-based Symbolic Simulation of Gate-level Synchronous Circuits" ACM (1999) pg. 1-6.			
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.